



asynchronous acknowledgement

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as:Patents **1 - 10** on asynchronous acknowledgement. (0.12 seconds)[Sort by relevance](#) | [Sort by date \(new first\)](#) | [Sort by date \(old first\)](#)[System for transmitting and](#)[receiving data within a reliable communications ...](#)

US Pat. 6345302 · Filed Aug 21, 2000 - TSI TelSys, Inc.

The method according to claim 18, for further reliably constructing an **asynchronous acknowledgement** to at least one received datagram, further comprising ...

[Low latency FIFO circuits for mixed asynchronous and synchronous systems](#)

US Pat. 6850092 · Filed Jun 8, 2001 - The Trustees of Columbia University

The **asynchronous acknowledgement** signals put ack and get ack are constructed as a tree of OR gates that merge individual acknowledgements into a single ...

[Software fault tolerant computer system](#)

US Pat. 6625751 · Filed Mar 14, 2000 - Sun Microsystems, Inc.

Optionally, in step 91, buffered input values spending **acknowledgement asynchronous** action, initialise can be output. In step S92, an end-of-epoch message ...

[Method and apparatus for extending the range of the universal serial bus ...](#)

US Pat. 6381666 · Filed Feb 18, 2000 - Igron Systems Inc.

... the requested **asynchronous** data from said peripheral device; ... said requested **asynchronous** data; ii) Generating a negative **acknowledgement** packet if ...

[Method and apparatus for transmitting data over data bus at maximum speed](#)

US Pat. 6397277 · Filed May 19, 1999 - Sony Corporation

If a suitable **acknowledgement** is received, subsequent data transmission to the recipient device, ie, **asynchronous** and/or isochronous, is performed at the ...

[Storage system making possible data synchronization confirmation at time of ...](#)

US Pat. 6591351 · Filed Sep 18, 2000 - Hitachi, Ltd.

Subsequently, the case where **asynchronous** remote copy 5 is being conducted ... As its **acknowledgement**, the storage system of the local side returns write ...

[Synchronous to asynchronous to synchronous interface](#)

US Pat. 6848060 · Filed Feb 27, 2001 - International Business Machines Corporation

Advantageously, the **asynchronous** intermediate 15 stages of circuit 10 are ... 2A
-B, the synchronous to **asynchronous** first stage local clock circuit 30 which ...

Asynchronous circuit for detecting and correcting soft error and ...

US Pat. 6476643 - Filed Oct 5, 2001 - STMicroelectronics S.A.

The operation of **asynchronous** circuits is based on the propagation of data and
... Finally control unit 3 sends the previous logic stage an **acknowledgement** ...

Model train control system

US Pat. 6270040 - Filed Apr 3, 2000 - KAM Industries


The **asynchronous** 55 processor 110 and issues appropriate control commands to the
... by the server and then an **acknowledgement** is returned to the client, ...

Information processing apparatus and method, and distribution medium

US Pat. 6754184 - Filed Jun 24, 1999 - Sony Corporation

In the **asynchronous** transmission, use of **acknowledgement** and rewrite ensures
reliable transmission, but timing of transmission is not fixed. ...

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